

ECN # 383.1

Page 1 of 2

**ENGINEERING CHANGE NOTE:**

mfx3

ECN #:

383.1

ASSEMBLY: BMW4011**DESCRIPTION:** MW4-PXY CPU CARD**New Assembly:****DATE:** 12/09/00**Current Assy Rev:** M**Current Schematic Rev:** 5.7**New Assy Rev:** N**New Schematic Rev:** 5.8

☐ CMS BOMs Updated ☐ Excel BOMs Updated ☐ Subcontractor Records Updated **Compatibility Maintained:** Yes

IMPLEMENTATION INFORMATION:

- ☐ Safety Issue ☐ UL Compliance Issue ☐ New Feature ☐ Cost Reduction
☐ Bug Fix ☐ EMC Compliance Issue ☐ Quality Issue
☐ Reliability Reasons ☐ Software Change ☐ Cosmetic Change

REASON FOR CHANGE:

1. Required for signal integrity of LEA, LEB on PX and PY dram.
2. To prevent the slow output of the resetcontroller U79 causing false transitions of the reset signal FLASHRSTn and fix the problem of PXYs not starting reliably.
3. U21 no longer used (JTAG controller now in INTGLUE).
4. Update revision codes.

DETAILS OF CHANGE:

1. Remove diodes at D15,D16,D17,D18,D19,D20,D21,D22.
2. Replace IC 74F04 at location U26 with a 14 pin SOIC IC 74HC14 (P/N SSS5878) .
3. At location C96 (10uF P/N CTY5173) increase the value to 100uF.
If the 10uF (A Case) part is loaded then add a 100uF 16v radial electrolytic cap (P/N CER4645) by solder directly to the existing 10uF part. (Retain correct polarity)
In future production, do not load the 10uF cap. Instead, glue a 100uF 16v radial electrolytic cap (P/N CER4645) to the PCB and solder the leads to the pads of C96. (Retain correct polarity)
4. U21 need not be loaded for all future boards. (Existing cards can leave U21 loaded)
5. Place revision resistors (RMD8573 - RES 10K 0.125W 1% SMD0603) as follows;
R127 - 10k
R128 - Omit
R133 - 10k
R134 - Omit
R135 - 10k
R136 - Omit
R137 - Omit
R138 - 10k

Place a assembly revision label on the card, marked "N", after the blank Rev 5 PCB P/N so it reads as " BMW4011E-N".

HW or Project Engineer.....

R&D or Project Manager.....

ECN # 383.1

Page 2 of 2

ECN383.1 is a correction to ECN 383: Added a change detail for the change reason 2.
